

INFORMATION DISCLOSURE CITATION IN AN APPLICATION							
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(PTO-1449)			APPLICANT Tadaaki YAMAUCHI				
			FILING DATE September 18, 2003	GROUP <div>2818</div>			
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codez (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number & Kind Codes (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
Hm		JP P2000-100186A	04/07/2000	NEC CORP		(Japan w/English Abstract)	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (In CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
Hm		"A 1-Mbit CMOS EPROM with Enhanced Verification", Roberto Gastaldi et al., IEEE Journal of Solid-State Circuits, Vol. 23, No. 5, October 1988, pp. 1150-1156					
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